

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	17	(microcontroller and breakpoint and ice and lock\$step)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/11/06 13:30
L2	2598	(ice) and pod	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/11/06 13:44
L3	590	L2 and (control same data)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/11/06 13:44
L4	140	L3 and (synchron\$lock\$step)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/11/06 13:44
L5	22	L4 and (break\$point)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/11/06 13:44
L6	118	4 not 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/11/06 13:51
L7	21	(in\$circuit\$emulat\$4) and (synchroniz\$5 lock\$step)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/11/06 14:00
L8	23	(in\$circuit\$emulat\$4) and (break\$point)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/11/06 14:04

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